

Box 111 Suite 1500  
438 University Avenue  
Toronto ON M5G 2K8 Canada  
Tel. 416.593.5514 | Fax 416.591.1690  
[www.smart-biggar.ca](http://www.smart-biggar.ca)

19 January 2011

Commissioner of Patents  
United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Re: United States Patent No. 5,867,358**  
**Patent Issued Date: February 2, 1999**  
Application No. 08/842,186  
Filing Date: April 23, 1997  
Inventor: Campbell, John S.  
**Title: FAULT CURRENT LIMITING CIRCUIT**

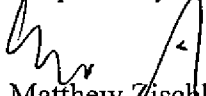
Please be advised that the entity status with respect to the above mentioned patent has changed from Large Entity to **Small Entity**. Accordingly, kindly update your records.

Concurrently, we are herewith submitting payment of the maintenance fee for the 12<sup>th</sup> year as Small Entity status together with a late surcharge fee.

**The total amount of the fees is \$2,120.00** which includes the 12<sup>th</sup> year maintenance fee in the amount of \$2,055.00 and \$65.00 for the surcharge.

If any additional fees are required please charge them to our deposit account.

Respectfully submitted,



Matthew Zischka  
Regn. No. 41,575  
Smart & Biggar  
438 University Ave.  
Suite 1500,  
Toronto, Ontario M5G 2K8  
416-593-5514  
[Toronto@smart-biggar.ca](mailto:Toronto@smart-biggar.ca)

Our Reference No. 91634-6